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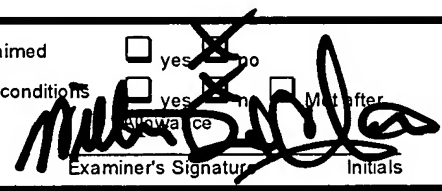
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\*\* CONTINUING DATA \*\*\*\*\* NONE 

\*\* FOREIGN APPLICATIONS \*\*\*\*\* NONE 

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
 \*\* 12/04/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no Verified and Acknowledged	<input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> Met after allowance Examiner's Signature  Initials	STATE OR COUNTRY CA	SHEETS DRAWING 2	TOTAL CLAIMS 19	INDEPENDENT CLAIMS 3
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ADDRESS

Intellectual Property Law Department  
 LSI Logic Corporation  
 1551 McCarthy Boulevard  
 Mail Stop D-106  
 Milpitas, CA  
 95035

TITLE

Apparatus and method of manufacture for integrated circuit and CMOS device including epitaxially grown dielectric on silicon carbide

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